

# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

**(Use as many sheets as necessary)**

Sheet	1	of	2
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**Complete if Known**

<b>Application Number</b>	<b>10/797,604</b>
<b>Filing Date</b>	<b>03/11/2004</b>
<b>First Named Inventor</b>	<b>Duk-Min Yi et al.</b>
<b>Art Unit</b>	<b>2826</b>
<b>Examiner Name</b>	<b>Kevin V. Quinto</b>
<b>Attorney Docket Number</b>	<b>SEC.1066</b>

## U. S. PATENT DOCUMENTS

[illegible]

**FOREIGN PATENT DOCUMENTS**

FOREIGN PATENT DOCUMENTS						
Examiner Initials*	Cite No. <sup>1</sup>	Foreign Patent Document	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages Or Relevant Figures Appear	T <sup>o</sup>
		Country Code <sup>3</sup> Number <sup>4</sup> Kind Code <sup>5</sup> (if known)				
H 72	B	JP 55113378	09/01/1980	Hitachi Ltd		✓
	C	JP 11261056	09/24/1999	Toshiba Corp		✓

**Examiner  
Signature**

Kevin Quinto

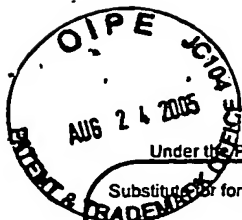
Date Considered

11/10/85

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PTO/SB/08B (07-05)

Approved for use through 07/31/2008. OMB 0851-0031

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Examiner Name	Kevin V. Quinto
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Sheet 2 of 2

**NON PATENT LITERATURE DOCUMENTS**

Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>
72	D	"Field-effect transistor output driver" IBM TECHNICAL DISCLOSURE BULLETIN, IBM CORP. NEW YORK, US, vol. 28, no. 8, 1986, pages 3497-3498, XP002088269	✓

Examiner Signature	Kevin Quinto	Date Considered	11/10/03
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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